

Form PTO-1449 (modified) List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Atty. Docket No. 10830.0109.NPUS00	Serial No.
	Applicant Jean-Pierre Bono	
	Filing Date:	Group:

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1	2003/0018691 A1	01/23/2003	Bono	709	106	06/29/2001
	A2	6,324,581	11/27/2001	Xu et al.	709	229	03/03/1999
	A3	6,289,369	09/11/01	Sundaresan	709	103	08/25/1998
	A4	6,269,390	07/31/01	Boland	709	100	12/17/1996
	A5	6,223,204	04/24/01	Tucker	709	103	12/18/1996
	A6	6,192,408	02/20/2001	Vahalia et al.	709	229	09/26/1997
	A7	5,987,621	11/16/1999	Duso et al.	714	4	05/05/1997
	A8	5,951,672	09/14/1999	Kwok et al.	712	28	07/02/1997
	A9	5,922,057	07/13/1999	Holt	710	52	01/10/1997
	A10	5,893,140	04/06/1999	Vahalia et al.	711	118	11/13/1996
	A11	5,826,081	10/20/98	Zolnowsky	395	673	05/06/1996
	A12	5,437,032	07/25/1995	Wolf et al.	395	650	08/19/1994
	A13	4,604,694	08/05/1986	Hough	364	200	12/14/1983

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C1	EMC Celerra SE5 File Server, EMC Corporation, Hopkinton, MA, 2002, 2 pages.
	C2	"Celerra File Server in the E-Infostructure," EMC Corporation, Hopkinton, MA, 2000, 9 pages.
	C3	"Celerra File Server Architecture for High Availability," EMC Corporation, Hopkinton, MA, 1999, pp. 1-7.

EXAMINER:**DATE CONSIDERED:**

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	C4	"MultiProcessor Specification Version 1.4," May 1997, Intel Corporation, Mt. Prospect, IL, 1993-1997, 1-1 to 5-8, A-1 to E-6.
	C5	Helen S. Raizen and Stephen C. Schwarm, "Building a Semi-Loosely Coupled Multiprocessor System Based on Network Process Extension," Prime Computer, Inc., Framingham, MA, Pre-Publication Copy, January 29, 1991, pp. 1-17.
	C6	Morioka et al., "Design and Evaluation of the High Performance Multi-Processor Server," IEEE International Conference on Computer Design: VLSI in Computers and Processors, IEEE Computer Society Press, Los Alamitos, CA 1994, pp. 66-69.
	C7	"Intel Xeon Processor: Unparalleled Value and Flexibility for Small and Medium Business Server Applications," Intel Corporation, Santa Clara, CA, 2002, 4 pages.
	C8	"Intel Server Board SE7500WV2," Intel Corporation, Santa Clara, CA, 2002, 6 pages.
	C9	"Building Cutting-Edge Server Applications: Intel Xeon Processor Family Features the Intel NetBurst Microarchitecture with Hyper-Threading Technology," White Paper, Intel Corporation, Santa Clara, CA, 2002, 10 pages.
	C10	"Panicel IMPI project: Summary," SourceForge.net, http://sourceforge.net/projects/panicel , printed March 12, 2004, 3 pages.
	C11	"Panic Handler Enhancements, Project Overview – panicel," SourceForge.net, http://sourceforge.net/home.html , printed Feb. 5, 2004, 2 pages.
	C12	Cress, Andrew, "Panic Handler Enhancements for Linux 2.4, High-Level Design," Intel Corporation, SourceForge.net, http://sourceforge.net , printed March 12, 2004, pp. 1-16.
	C13	Cox, Alan, "Watchdog Timer Interfaces For the Linux Operating System," http://stommel.tamu.edu , Texas A&M University, College Station, TX, printed Feb. 5, 2004, 2 pages.
	C14	Lynch, Rusty, "Patch: Proposal for a new watchdog interface using sysfs," LWN.net, printed Feb. 5, 2004, 19 pages.
	C15	Dennis, James T., "Watchdog daemon," www.linuxgazette.com/issue78 , printed Feb. 5, 2004, 3 pages.
	C16	"Spinlock," http://en.wikipedia.org , Wikipedia, printed Feb. 5, 2004, 1 page.

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	C17	"Deadlock detected waiting for spinlock," Technical Information Document TID 10072683, http://support.novell.com , Novell Inc., Provo, UT, printed Feb. 5, 2004, 2 pages.
	C18	Doelle, Juergen, "Improving spinlock contention for dbench, 2 nd version," www.geocrawler.com , printed Feb. 5, 2004, 8 pages.
	C19	"Linux spinlock.h, generic versions of the spinlocks and read-write locks," code listing, http://www2.randomlogic.com , printed Feb. 5, 2004, 4 pages.

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